

# *Lambda and pi calculi, CAM and SECD machines*

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## Abstract

We analyse machines that implement the call-by-value reduction strategy of the  $\lambda$ -calculus: two environment machines – CAM and SECD – and two encodings into the  $\pi$ -calculus – due to Milner and Vasconcelos. To establish the relation between the various machines, we setup a notion of reduction machine and two notions of correspondences: operational – in which a reduction step in the source machine is mimicked by a sequence of steps in the target machine – and convergent – where only reduction to normal form is simulated. We show that there are operational correspondences from the  $\lambda$ -calculus into CAM, and from CAM and from SECD into the  $\pi$ -calculus. Plotkin completes the picture by showing that there is a convergent correspondence from the  $\lambda$ -calculus into SECD.

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## Capsule Review

Vasconcelos brings an interesting new perspective to the relationship between the call-by-value  $\lambda$ -calculus and its encodings into the  $\pi$ -calculus. The present paper studies some of the existing informative intuitions behind these encodings in a methodical way. First, a framework of general reduction machines is defined. And call-by-value  $\lambda$ -calculus, its two abstract machines (called CAM and SECD), and two  $\pi$ -calculus machines (called Process and Context) are given in terms of a reduction machine. Then, a correspondence based on Milner's encoding is shown between CAM and Process, and a correspondence based on author's existing encoding is shown between SECD and Context. The correspondences clearly point out the difference between the two encodings, too. The relation between  $\lambda$ -calculus and its two abstract machines is summarised as well. In the end, the paper leaves only one relation unstudied: the relation between the two encodings in the context of the  $\pi$ -calculus.

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## 1 Introduction

The call-by-value reduction strategy of the  $\lambda$ -calculus equips a large number of today's programming languages. To study the operational aspects of this reduction strategy Plotkin used Landin's SECD, a stack-based, environment machine (Landin, 1964; Plotkin, 1975). Based on the Categorical Abstract Machine and inspired by the Krivine's lazy abstract machine, Curien proposed a different environment machine, which we call CAM throughout this paper (Curien, 1991). From a different community, two interpretations of the call-by-value  $\lambda$ -calculus into the  $\pi$ -calculus are known: one proposed by Milner, and further studied by Pierce & Sangiorgi (Milner,

1990; Milner, 1992; Pierce & Sangiorgi, 1996), the other proposed by the author, but to date lacking a systematic study (Vasconcelos, 1994).

In his paper on encodings of the  $\lambda$ -calculus into the  $\pi$ -calculus, Milner uses the term ‘environment entry’ to describe a process simulating the behaviour of some function on a given channel (Milner, 1990; Milner, 1992). The term comes probably from the so called environment machines, devices that describe implementations of the  $\lambda$ -calculus, capable of efficiently performing substitutions by maintaining a map from variables to function closures (the environment). The natural question arises, “What is the relation between the  $\pi$ -encodings of the  $\lambda$ -calculus and the environment machines?”. We answer the question by showing that there is a close correspondence (indeed, an operational correspondence) between the CAM machine and Milner’s encoding, and between the SECD machine and current author’s encoding. Also, because there is an operational correspondence from the  $\lambda$ -calculus into CAM, but no such correspondence into SECD learn that the two encodings are quite different. In fact, our study reveals:

1. How close the  $\pi$ -encodings are to the environment machines: Milner’s encoding mimics step-by-step the CAM machine; Vasconcelos’ does the same for the SECD machine;
2. The inherent difference between the two encodings: Milner’s follows the reduction of a  $\lambda$ -term step-by-step; Vasconcelos’ cannot follow intermediate steps.

To establish the correspondences between the various machines, we setup a notion of *reduction machine* – composed of a set of states, a reduction relation, and an equivalence relation, satisfying some simple conditions – and a notion of *correspondence* – a partial function on states that preserves equivalence. We identify two kinds of correspondences: *operational* where a reduction step in the source machine is mimicked by a sequence of steps in the target machine, modulo equivalence; and *convergent* where reduction to normal form in the source machine is mimicked by reduction to normal form in the target machine, again modulo equivalence. Reduction machines and correspondences are then studied on their own, paving way for the results on the particular correspondences studied in the paper.

Equipped with the notion of reduction machines we set up five concrete machines: the usual  $\lambda$ -calculus with the call-by-value reduction strategy (Plotkin, 1975), the CAM machine (Curien, 1991), the SECD machine (Plotkin, 1975), and two  $\pi$ -calculus based machines. For the first  $\pi$ -based machine, we pick processes of the asynchronous  $\pi$ -calculus (Boudol, 1992; Honda & Tokoro, 1991) typable under the input/output type system, together with deterministic reduction, and strong barbed congruence (Pierce & Sangiorgi, 1996).

For the second  $\pi$ -based machine, we pick contexts for states. A notion of reduction and equivalence for  $\pi$ -calculus contexts is then defined. A (typed) context performs a (deterministic) reduction step if the (typed) process in the hole plays no rôle in reduction, that is, if the reduction step of the filled-in context is independent of the process that fills the hole. For the equivalence, we isolate a subset of names that

cannot be free in the hole. Then, we play the same game as for reduction: two contexts are strong barbed congruent if the processes obtained by filling the hole with the same process are strong barbed congruent. In our particular case, the set of names that cannot be free in the hole corresponds to the  $\lambda$ -variables (which form a subset of the  $\pi$ -names).

We then study four correspondences: two from the call-by-value  $\lambda$ -calculus into the environment machines (CAM and SECD), another two from each environment machine into a  $\pi$ -based machine. The encoding of the CAM machine into the  $\pi$ -calculus is based on a variant of Milner’s encoding (Milner, 1990; Pierce & Sangiorgi, 1996; Sangiorgi & Walker, 2001), obtained by partially evaluating applications whose left operand (the function) is already a value. The encoding of the SECD machine into  $\pi$ -calculus contexts is based on current author’s encoding (Vasconcelos, 1994; Vasconcelos, 2000). The advantage of proceeding through environment machines is that proofs of the operational correspondences are quite simple: in each case it resumes to a simple analysis of the reduction rules, appealing directly to the Replication Theorems (Pierce & Sangiorgi, 1996; Sangiorgi & Walker, 2001).

We also study direct interpretations of the  $\lambda$ -calculus into the  $\pi$ -calculus, and show that they are sound. The direct encodings allow to quantify the number of  $\pi$ -reduction steps needed to mimic a  $\lambda$ -step: exactly two for the variant of the Milner’s encoding (but not for the original version), and two (in average, for one can only compare values) in the case of the context encoding.

As mentioned above, the two encodings are quite different, for one reflects the CAM machine, while the other the SECD machine. The process-machine mimics, one-by-one, the  $\lambda$ -steps of a term (Theorem 32):

$$M \rightarrow_v N \text{ implies } \llbracket M \rrbracket \rightarrow_d^2 \simeq \llbracket N \rrbracket.$$

The context translation mimics  $\lambda$ -reduction to a value (theorem 34),

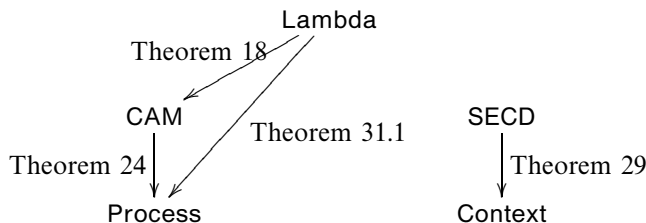
$$M \downarrow_v^n N \text{ implies } \llbracket M \rrbracket \downarrow_d^{2n} \simeq \llbracket N \rrbracket.$$

but cannot follow the intermediate steps:

$$(\lambda x \mathbf{I}x)V \rightarrow_v \mathbf{I}V, \text{ but } \llbracket (\lambda x \mathbf{I}x)V \rrbracket \not\rightarrow_d^* \simeq \llbracket \mathbf{I}V \rrbracket.$$

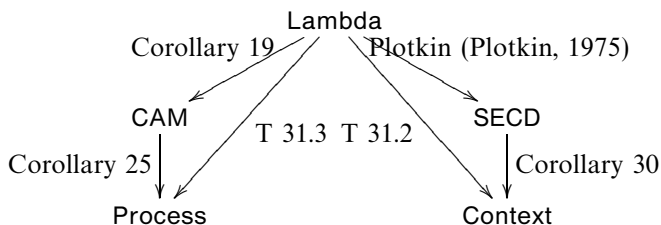
**The big picture**

The five machines studied in this paper are Lambda (section 3.1), CAM (section 3.2), SECD (section 3.3), Process (section 3.5), and Context (section 3.6). For  $M_1, M_2$  two machines, we write  $M_1 \rightarrow M_2$  if there is a correspondence from  $M_1$  to  $M_2$ . For the operational case, we have the following scenario.



The Lambda to CAM to Process operational correspondences are the object of sections 4.1, and 4.3. Then, we may obtain the Lambda to Process correspondence by composition (section 4.5). In a different partition of the diagram, the SECD to Context machine is the object of section 4.4.

For the *convergent* case, Plotkin provides the missing link, thus completing the puzzle.



The arrows in the operational diagram are inherited, since operational correspondences are also convergent (theorem 5). The Lambda to SECD correspondence is the object of section 4.2, the result is by Plotkin (Plotkin, 1975). Then, we bridge the Lambda and the Context machines via composition (section 4.5).

The next section introduces the notions of reduction machine and correspondence, together with basic results on these. Section 3 introduces the five machines mentioned above, and section 4 studies the correspondences. Section 5 concludes the paper.

## 2 Correspondences

This section introduces the notion of reduction machine, and two kinds of correspondences between them. *Reduction machines* are given by a set of states, a transition relation on states, and an equivalence relation also on states.

*Definition 1 (Reduction machine)*

A *reduction machine* is a triple  $\langle S, \rightarrow, \sim \rangle$ , where  $S$  is a set (the *set of states*),  $\rightarrow$  is a relation in  $S \times S$  (the *reduction relation*), and  $\sim$  is an equivalence relation in  $S \times S$ , such that

1.  $s \rightarrow s', s \rightarrow s''$  implies  $s' \sim s''$ , and
2. the relation  $\sim \rightarrow$  is contained in the relation  $\rightarrow^* \sim$ .

Notice that we do not require that  $\sim$  and  $\rightarrow$  commute, not even that  $\sim \rightarrow$  is contained in  $\rightarrow \sim$ . Both the CAM and the SECD reduction machines defined in sections 3.2, 3.3 do not satisfy the latter condition. Also, a simple induction on the length of the reduction shows that  $\sim \rightarrow^*$  is contained in  $\rightarrow^* \sim$  as well.

For a given machine  $\langle S, \rightarrow, \sim \rangle$ , we say that: (a) state  $s$  *reduces*, and write  $s \rightarrow$ , to mean  $s \rightarrow s'$  for some  $s'$ ; (b) state  $s$  *reduces in  $n$  steps* to state  $s'$  ( $n \geq 0$ ), and write  $s \rightarrow^n s'$ , if  $s \rightarrow s_1 \rightarrow \dots \rightarrow s_n = s'$ ; when  $n$  is not important we write  $s \rightarrow^* s'$ ; (c) state  $s$  *converges in  $n$  steps* to  $s'$  ( $n \geq 0$ ), and write  $s \downarrow^n s'$ , if  $s \rightarrow^n s' \nrightarrow$ ; when  $n$  is not important, we write  $s \downarrow s'$ ; and when  $s'$  is also not important, we write  $s \downarrow$ .

*Correspondences* are functions from machine states into machine states that preserve state equivalence. We distinguish two kinds of correspondences.

*Definition 2 (Correspondences)*

Given two reduction machines  $\langle S, \rightarrow_S, \sim_S \rangle$ ,  $\langle R, \rightarrow_R, \sim_R \rangle$ , and a partial function  $\text{load} : S \rightarrow R$ , we say that

1.  $\text{load}$  is a *correspondence* if  $s \sim s'$  implies  $\text{load}(s) \sim \text{load}(s')$ ;
2.  $\text{load}$  is *operational* if it is a correspondence, and
  - (a)  $s \rightarrow s'$  implies  $\text{load}(s) \rightarrow^* \sim \text{load}(s')$ ;
  - (b)  $\text{load}(s) \downarrow$  implies  $s \downarrow$ ; and
  - (c)  $\text{load}(s) \rightarrow$  implies  $s \rightarrow$ .
3.  $\text{load}$  is *convergent* if it is a correspondence, and
  - (a)  $s \downarrow s'$  implies  $\text{load}(s) \downarrow \sim \text{load}(s')$ ; and
  - (b)  $\text{load}(s) \downarrow$  implies  $s \downarrow$ .

Operational correspondences are not adequate, but, given a mild condition on the target machine, the correspondences become adequate.

*Proposition 3*

Given two reduction machines  $\langle S, \rightarrow_S, \sim_S \rangle$ ,  $\langle R, \rightarrow_R, \sim_R \rangle$  such that  $r \sim_R r' \not\rightarrow_R$  implies  $r \not\rightarrow_R$ , and an operational correspondence  $\text{load} : S \rightarrow R$ ,

1.  $s \rightarrow^* s'$  implies  $\text{load}(s) \rightarrow^* \sim \text{load}(s')$ ;
2.  $s \downarrow s'$  implies  $\text{load}(s) \downarrow \sim \text{load}(s')$ ;
3. (*Adequacy*)  $s \downarrow$  iff  $\text{load}(s) \downarrow$ .

*Proof*

- (1) Induction on the length of reduction. When  $s = s'$ , we know that  $\text{load}(s) = \text{load}(s')$ . Otherwise suppose that  $s \rightarrow^* s' \rightarrow s''$ . By induction we know that  $\text{load}(s) \rightarrow^* \sim \text{load}(s')$ , and from the definition of operational correspondence, that  $\text{load}(s') \rightarrow^* \sim \text{load}(s'')$ . Conclude with the fact that  $\sim \rightarrow^*$  is contained in  $\rightarrow^* \sim$ .
- (2) Use the above clause and the contrapositive of 2.2c to obtain  $\text{load}(s) \rightarrow^* r \sim \text{load}(s')$ , for some  $r$ . Use the hypothesis to conclude that  $r \rightarrow$ .
- (3) Clause above and definition 2.2b.  $\square$

Adequacy in the convergence scenario is easier to establish.

*Proposition 4*

Given two reduction machines  $\langle S, \rightarrow_S, \sim_S \rangle$ ,  $\langle R, \rightarrow_R, \sim_R \rangle$ , and a convergence correspondence  $\text{load} : S \rightarrow R$ ,

1.  $\text{load}(s) \downarrow r$  implies  $s \downarrow s'$  and  $r \sim \text{load}(s')$ ;
2.  $s \downarrow$  implies  $\text{load}(s) \downarrow$ ;
3. (*Adequacy*)  $s \downarrow$  iff  $\text{load}(s) \downarrow$ .

*Proof*

- (1), (2) Directly from the definition.
- (3) Definition and clause 2.  $\square$

The main result concerning correspondences says that operational correspondences are also convergence correspondences, and that the composition of correspondences is also a correspondence.

	States: term	
variable	$u, v, w, x, y, z$	
value	$V ::= x \mid \lambda x M$	
term	$M ::= V \mid (MN)$	
	Reduction: $\rightarrow_v$	
	$(\lambda x M)V \rightarrow_v M\{V/x\}$	( $\beta$ )
	$VM \rightarrow_v VM'$ if $M \rightarrow_v M'$	( $\nu$ )
	$MN \rightarrow_v M'N$ if $M \rightarrow_v M'$	( $\mu$ )
	Equivalence: $\equiv_x$	

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Fig. 1. The call-by-value Lambda machine.

*Theorem 5*

1. Operational correspondences are convergent;
2. The composition of two operational correspondences is operational;
3. The composition of two convergence correspondences is convergent;
4. The composition of a convergent with an operational correspondence is convergent.

*Proof*

(1) Proposition 3.2 and definition 2.2b. (2) The second and the third clauses in the definition of operational correspondences are direct. For the first, let  $R, S, T$  be the three machines involved. Using proposition 3.1 on  $R$ , we have  $\text{load}(s) \rightarrow_R^* r \sim_R \text{load}(s')$ ; using the same proposition on  $S$ , we have  $\text{load}(\text{load}(s)) \rightarrow_T^* \sim_T \text{load}(r)$ . By definition of correspondence  $\text{load}(r) \sim_T \text{load}(\text{load}(s'))$ ; hence  $\text{load}(\text{load}(s)) \rightarrow_T^* \sim_T \text{load}(\text{load}(s'))$ , as required. (3) Easy. (4) Clauses 1 and 3 above.  $\square$

### 3 Five reduction machines

This section presents the following machines, all taken from the literature: the  $\lambda$ -calculus equipped with the call-by-value strategy, the CAM machine, the SECD machine, and two machines based on the  $\pi$ -calculus.

#### 3.1 The call-by-value machine

This section introduces Plotkin's call-by-value  $\lambda$ -calculus (Plotkin, 1975). We presuppose a countable set `variable` of variables. The sets of values and terms are defined in figure 1. We say that a variable  $x$  occurs *free* in a term  $M$  if  $x$  is not in the scope of a  $\lambda x$ ;  $x$  occurs *bound* otherwise. The set  $\text{fv}(M)$  of the *free variables* in  $M$  is defined accordingly, and so is the result of substituting  $N$  for the free occurrences of  $x$  in  $M$ , denoted by  $M\{N/x\}$ , as well as the *alpha equivalence*, denoted by  $\equiv_x$ . A term  $M$  is closed if  $\text{fv}(M) = \emptyset$ . The set of closed  $\lambda$ -terms is denoted by  $\text{term}^0$ . The *reduction relation* over `term`, written  $\rightarrow_v$ , is the smallest relation satisfying the rules in figure 1.

The  $\rightarrow_v$  relation is Plotkin's left reduction: "If  $M \rightarrow_v N$ , then  $N$  is gotten from  $M$  by reducing the leftmost redex, not in the scope of a  $\lambda$ " (Plotkin, 1975). Notice

that, since states are closed terms, saying that  $M \downarrow^n N$  amounts to say that  $M \rightarrow^n N$  and  $N$  is a value.

The reduction machine we are interested in operates on closed terms and is defined in figure 1.

*Proposition 6*

The triple  $\langle \text{term}^0, \rightarrow_v, \equiv_\alpha \rangle$  is a reduction machine.

*Proof*

For the first clause in definition 1 we know that  $s' = s''$ . For the second,  $\rightarrow_v$  commutes with  $\equiv_\alpha$ .  $\square$

*Example 7*

Consider the term  $(\lambda x \mathbf{I}x)V$ , where  $V$  is a closed value, and  $\mathbf{I}$  the  $\lambda y y$  combinator. We have:

$$(\lambda x \mathbf{I}x)V \rightarrow_v \mathbf{I}V \rightarrow_v V.$$

### 3.2 The CAM machine

This section introduces the CAM machine (Curien, 1991), following the presentation of Amadio & Curien (1998). We start with environments and closures, two notions mutually defined; closure is the set of closures, and environment the set of environments. An *environment* is a partial function of finite domain from variables into closures. A *closure*  $c$  is a pair  $M[e]$  in  $\text{term} \times \text{environment}$ . We evaluate closures  $M[e]$  such that  $\text{fv}(M) \subseteq \text{dom}(e)$ . Notice that an empty environment constitutes the base of the recursive definition: if  $M$  is a closed term, then  $M[\emptyset]$  is a closure. *Environment update* is captured by the operation  $e\{x := vc\}$ , denoting the unique environment  $e'$  such that  $e'(y) = e(y)$  if  $y \neq x$ , and  $e'(x) = vc$  otherwise.

The CAM machine uses a stack to keep track of terms waiting to be evaluated or waiting for their arguments to be evaluated. In order to distinguish what is on top of the stack, the machine uses two markers,  $l$  and  $r$ , specifying whether the next term in the stack is the left side of an application (function) or the right side (argument). Elements in the stack are separated by a colon, the top of the stack is at the left; the empty stack is denoted by  $\text{nil}$ . We often omit the trailing  $: \text{nil}$  in a stack.

The CAM machine is defined in figure 2: the reduction function, written  $\rightarrow_k$ , is the smallest relation satisfying the rules in the figure; the equivalence relation on states, written  $\sim_k$ , is the smallest equivalence relation that contains the equalities in the same figure.

Given a closed term  $M$ , the machine starts with state  $\langle M[\emptyset], \text{nil} \rangle$ . We can easily check that terminal states are of the form  $\langle vc, \text{nil} \rangle$ .

*Example 8*

Recall the term  $(\lambda x \mathbf{I}x)V$  from example 7. We have:

$$\begin{aligned} \langle (\lambda x \mathbf{I}x)V [], \text{nil} \rangle &\rightarrow && \text{(APP)} \\ \langle (\lambda x \mathbf{I}x) [], r : V [] \rangle &\rightarrow && \text{(EXCH)} \end{aligned}$$

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States: cam	
environment	$e ::= \{x_1 := vc_1, \dots, x_n := vc_n\} \quad n \geq 0, x_i \text{ distinct}$
valueclosure	$vc ::= (\lambda x M)[e] \quad \text{if } \text{fv}(\lambda x M) \subseteq \text{dom}(e)$
closure	$c ::= M[e] \quad \text{if } \text{fv}(M) \subseteq \text{dom}(e)$
stack	$s ::= l : vc : s \mid r : c : s \mid \text{nil}$
cam	$k ::= \langle c, s \rangle$
Reduction: $\rightarrow_k$	
	$\langle x[e], s \rangle \rightarrow_k \langle e(x), s \rangle \quad (\text{VAR})$
	$\langle (MN)[e], s \rangle \rightarrow_k \langle M[e], r : N[e] : s \rangle \quad (\text{APP})$
	$\langle vc, r : c : s \rangle \rightarrow_k \langle c, l : vc : s \rangle \quad (\text{EXCH})$
	$\langle vc, l : (\lambda x M)[e] : s \rangle \rightarrow_k \langle M[e\{x := vc\}], s \rangle \quad (\text{CALL})$
Equivalence: $\sim_k$	
	$\langle (VN)\square, s \rangle \sim_k \langle N\square, l : V\square : s \rangle$
	$\langle (MN)\square, s \rangle \sim_k \langle M\square, r : N\square : s \rangle$
	$\langle M[x := V\square], s \rangle \sim_k \langle (M\{V/x\})\square, s \rangle$

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Fig. 2. The CAM reduction machine.

$$\begin{aligned}
 \langle V\square, l : (\lambda x \mathbf{I}x)\square \rangle &\rightarrow && (\text{CALL}) \\
 \langle (\mathbf{I}x)[x := V\square], \text{nil} \rangle &\rightarrow^3 && (\text{APP, EXCH, VAR}) \\
 \langle V\square[x := V\square], l : I[x := V\square] \rangle &\rightarrow && (\text{CALL}) \\
 \langle y[y := V\square, x := V\square], \text{nil} \rangle &\rightarrow && (\text{VAR}) \\
 \langle V\square, \text{nil} \rangle &&&
 \end{aligned}$$

Notice that the state  $\langle (\mathbf{I}x)[x := V\square], \text{nil} \rangle$  above is equivalent to  $\langle \mathbf{I}V\square, \text{nil} \rangle$ , hence  $\langle (\lambda x \mathbf{I}x)V\square, \text{nil} \rangle \rightarrow_k^* \sim \langle \mathbf{I}V\square, \text{nil} \rangle \rightarrow_k^* \langle V\square, \text{nil} \rangle$  (cf. example 7). In section 4.1 we show that there is an operational correspondence from Lambda to CAM.

*Proposition 9*

The triple  $\langle \text{cam}, \rightarrow_k, \sim_k \rangle$  is a reduction machine.

*Proof*

For the first clause in definition 1 notice that  $s' = s''$ . For the second, a routine inspection of the rules shows that  $s \sim_k \rightarrow_k s'$  implies  $s \rightarrow_k^* s'$ .  $\square$

All reduction steps but CALL are administrative: looking up values for variables in the environment (VAR), and descending on a term, looking for the next redex (APP, EXCH). The following result says that, on each run of the CAM machine, the number of consecutive administrative steps is finite. Such a result is used in the CAM to Process encoding (section 4.3) to establish the second clause in the definition of operational correspondence (2.2).

*Lemma 10*

The CAM machine without the CALL rule terminates on every input.



*Proof*

Let  $k$  be the state  $\langle M_0[e_0], rl_1 : M_1[e_1] : \dots rl_k : M_k[e_k] : nil \rangle$ , where  $rl$  denotes an  $r$  or an  $l$ . Define the size of  $k$  as follows.

$$\begin{aligned} \text{size}(k) &\stackrel{\text{def}}{=} \text{size}\{M_0, \dots, M_k\} + \text{size}\{e_0, \dots, e_k\} + \sum_{1 \leq i \leq k} \text{size}(rl_i) \\ \text{size}(A) &\stackrel{\text{def}}{=} \sum_{a \in A} \text{size}(a) \\ \text{size}(l) &\stackrel{\text{def}}{=} 1 \\ \text{size}(r) &\stackrel{\text{def}}{=} 2 \\ \text{size}(MN) &\stackrel{\text{def}}{=} 3 + \text{size}(M) + \text{size}(N) \\ \text{size}(\lambda x M) &\stackrel{\text{def}}{=} 1 + \text{size}(M) \\ \text{size}(x) &\stackrel{\text{def}}{=} 1 \\ \text{size}(e) &\stackrel{\text{def}}{=} \sum_{x := M[e'] \in e} \text{size}(M) + \text{size}(e') \end{aligned}$$

It is easy to see that the size of a state decreases for each application of rules VAR, APP, and EXCH.  $\square$

### 3.3 The SECD machine

This section presents the SECD machine as studied by Plotkin (Plotkin, 1975). Machine states, or dumps, are quadruples  $\langle s, e, C, D \rangle$  composed of a stack of closures, an environment, a control string, and a dump. The sets of closures and environments are those of the CAM machine (figure 2).

The *stack* is used in the SECD machine to hold value closures only: evaluated functions and arguments. The rôle of the stack in the CAM machine is played here by the control string: it holds functions and arguments waiting for evaluation.

Values at the head of the control string get transferred to the stack. Since the stack contains only value closures, rule VAR looks in the environment for the value closure associated to the variable at the head of the stack, and pushes it into the stack. On the other hand, rule ABS forms a value closure with the abstraction at the top of the stack and the current environment, and pushes the closure into the stack. Rule APP replaces an application  $MN$  at the head of the control string by  $M, N$ , and  $ap$ , meaning that  $M$  and  $N$  should be evaluated in that order, and then rule CALL should be used. The  $ap$  mark at the head of the control string triggers rule CALL. At the top of the stack one finds the argument (since it was evaluated last), and, just beneath, one finds the function. The machine gets ready for the evaluation of a new term (the body of the function) with an empty stack (meaning no subterms evaluated so far), an updated environment, the body of the function, and for dump, “what remains to be done”.

$$\begin{array}{l}
\text{States: dump} \\
\text{stack } s ::= vc : s \mid \text{nil} \\
\text{controlstring } C ::= M : C \mid \text{ap} : C \mid \text{nil} \\
\text{dump } D ::= \langle s, e, C, D \rangle \mid \text{nil} \\
\text{Reduction: } \rightarrow_s \\
\langle s, e, x : C, D \rangle \rightarrow_s \langle e(x) : s, e, C, D \rangle \quad (\text{VAR}) \\
\langle s, e, \lambda x M : C, D \rangle \rightarrow_s \langle (\lambda x M)[e] : s, e, C, D \rangle \quad (\text{ABS}) \\
\langle s, e, MN : C, D \rangle \rightarrow_s \langle s, e, M : N : \text{ap} : C, D \rangle \quad (\text{APP}) \\
\langle vc : (\lambda x M)[e'] : s, e, \text{ap} : C, D \rangle \rightarrow_s \langle \text{nil}, e' \{x := vc\}, M, \langle s, e, C, D \rangle \rangle \quad (\text{CALL}) \\
\langle vc : \rightarrow, \text{nil}, \langle s, e, C, D \rangle \rangle \rightarrow_s \langle vc : s, e, C, D \rangle, \quad (\text{RET}) \\
\text{Equivalence: } \sim_s \\
\langle vc, \emptyset, \text{nil}, \text{nil} \rangle \sim_s \langle \text{nil}, \emptyset, \text{real}(vc), \text{nil} \rangle
\end{array}$$

Fig. 3. The SECD reduction machine.

To extract the term contained in a closure, we use the *real* function (Plotkin, 1975).

$$\text{real}(M[e]) \stackrel{\text{def}}{=} M\{\text{real}(e(x_1))/x_1\} \dots \{\text{real}(e(x_n))/x_n\}$$

where  $\text{fv}(M) = \{x_1, \dots, x_n\}$ .

The reduction function over dump, written  $\rightarrow_s$ , is the smallest relation satisfying the rules in figure 3. With respect to Plotkin's formulation we have changed rules APP and CALL, so that the function gets evaluated prior to the argument, as required by the reduction we have chosen for the call-by-value  $\lambda$ -calculus (section 3.1). The equivalence relation over dump, written  $\sim_s$ , is the smallest equivalence relation containing the equality in figure 3.

Given a closed term  $M$ , the machine starts with the dump  $\langle \text{nil}, \emptyset, M, \text{nil} \rangle$ . It is easy to check that terminal states are of the form  $\langle vc, \emptyset, \text{nil}, \text{nil} \rangle$ .

#### Example 11

Recall the term  $(\lambda x \mathbf{I}x)V$  from example 7. We have:

$$\begin{array}{l}
\langle \text{nil}, \emptyset, (\lambda x \mathbf{I}x)V, \text{nil} \rangle \rightarrow_s^3 \quad (\text{APP, ABS, ABS}) \\
\langle V[\emptyset] : \lambda x \mathbf{I}x[\emptyset], \emptyset, \text{ap}, \text{nil} \rangle \rightarrow_s^4 \quad (\text{CALL, APP, VAR, ABS}) \\
\langle V[e] : \mathbf{I}[e], e, \text{ap}, \langle \text{nil}, \emptyset, \text{nil}, \text{nil} \rangle \rangle \rightarrow_s^2 \quad (\text{CALL, VAR}) \\
\langle V[e], e\{y := V[e]\}, \text{nil}, \langle \text{nil}, e, \text{nil}, \langle \text{nil}, \emptyset, \text{nil}, \text{nil} \rangle \rangle \rangle \rightarrow_s \quad (\text{RET}) \\
\langle V[e], e, \text{nil}, \langle \text{nil}, \emptyset, \text{nil}, \text{nil} \rangle \rangle \rightarrow_s \quad (\text{RET}) \\
\langle V[e], \emptyset, \text{nil}, \text{nil} \rangle \sim_s \\
\langle \text{nil}, \emptyset, V, \text{nil} \rangle
\end{array}$$

where  $e$  is the environment  $\{x := V[\emptyset]\}$ . Notice that although  $(\lambda x \mathbf{I}x)V \rightarrow_v \mathbf{I}V$ , there is no state  $D$ , such that  $D \sim_s \langle \text{nil}, \emptyset, (\mathbf{I}V)[], \text{nil} \rangle$  (cf. example 8). In section 4.2 we show that there the Lambda to SECD correspondence is convergent (but not operational).

The property of being halted is not preserved by equivalence: there are equivalent dumps  $D$  and  $D'$ , such that  $D$  is halted but  $D'$  reduces (take  $\langle \mathbf{I}[\emptyset], \emptyset, \text{nil}, \text{nil} \rangle$  for  $D$ , and  $\langle \text{nil}, \emptyset, \mathbf{I}, \text{nil} \rangle$  for  $D'$ ). The rule in the equivalence relation is needed in order to

show that Lambda to SECD is convergent (Theorem 20). We have therefore chosen not to incorporate such a (natural) condition in the definition of reduction machine (definition 1). The condition is required on the target machine only; and the SECD machine is involved as source in its main result (Theorem 29).

*Proposition 12*

The triple  $\langle \text{dump}, \rightarrow_s, \sim_s \rangle$  is a reduction machine.

*Proof*

For the first clause in definition 1 we have that  $s' = s''$ . For the second, given that dumps of the form  $\langle vc, \emptyset, \text{nil}, \text{nil} \rangle$  do not reduce, the interesting case is when  $D = \langle \text{nil}, \emptyset, \text{real}(vc), \text{nil} \rangle \sim_s D'$ . The only rule applicable to  $D'$  is ABS, yielding  $D' \rightarrow_s D$ . Since  $\sim_s$  is an equivalence relation, we have  $D \sim_s D$ .  $\square$

All reduction steps but CALL and RET are administrative: looking values for variables in the environment (VAR), moving values from the control string into the stack (ABS), and breaking applications in the control string (APP). The following result says that, on each run of the SECD machine, the number of consecutive administrative steps is finite. Such a result is used in the SECD to Context encoding (section 4.4) to establish the second clause in the definition of operational correspondence (2.2).

*Lemma 13*

The SECD machine without rules CALL and RET terminates on every input.

*Proof*

Let  $D$  be the dump  $\langle V_1[e_1] : \dots : V_n[e_n] : \text{nil}, e_0, C, D' \rangle$ . Define the size of a dump as follows.

$$\begin{aligned} \text{size}(D) &\stackrel{\text{def}}{=} \sum \text{size}(V_i) + \text{size}(\{e_0, \dots, e_n\}) + \text{size}(C) + \text{size}(D') \\ \text{size}(\text{nil}) &\stackrel{\text{def}}{=} 0 \\ \text{size}(M : C) &\stackrel{\text{def}}{=} 1 + \text{size}(M) + \text{size}(C) \\ \text{size}(\text{ap} : C) &\stackrel{\text{def}}{=} 1 + \text{size}(C) \\ \text{size}(MN) &\stackrel{\text{def}}{=} 1 + \text{size}(M) + \text{size}(N) \end{aligned}$$

The remaining cases (set,  $\lambda xM$ ,  $x$ , and  $e$ ) are as in the proof of lemma 10. It is easy to see that the size of a dump decreases for each application of rules VAR, APP, and ABS.  $\square$

### 3.4 Typed $\pi$ -calculus

The  $\pi$ -calculus equipped with input/output types constitutes the basis of the next two machines. Typed processes allow for a stronger form of the replication theorems (Theorem 15 below), necessary to prove the correspondences (Pierce & Sangiorgi, 1996). The syntax of its asynchronous version is defined in figure 4, where the set  $\text{name} \setminus \text{variable}$  is countable.

name	Processes	
$p, q, r, u, v, w, x, y, z$	(variable $\subset$ name)	
process	$P ::= \bar{u}\tilde{v} \mid u(\tilde{x}).P \mid P \mid Q \mid \nu x P \mid !u(\tilde{x}).P \mid \mathbf{0}$	
	Structural congruence	
	$P \equiv Q$ if $P \equiv_{\alpha} Q$	
	$P \mid \mathbf{0} \equiv P, P \mid Q \equiv Q \mid P, P \mid (Q \mid R) \equiv (P \mid Q) \mid R$	
	$\nu x \mathbf{0} \equiv \mathbf{0}, \nu xy P \equiv \nu yx P$	
	$P \mid \nu x Q \equiv \nu x (P \mid Q)$ if $x \notin \text{fn}(P)$	
	Reduction	
$\bar{u}\tilde{v} \mid u(\tilde{x}).P$	$\rightarrow P\{\tilde{v}/\tilde{x}\}$	(COM)
$\bar{u}\tilde{v} \mid !u(\tilde{x}).P$	$\rightarrow !u(\tilde{x}).P \mid P\{\tilde{v}/\tilde{x}\}$	(REP)
	$\nu x P \rightarrow \nu x P'$ if $P \rightarrow P'$	(SCOP)
	$P \mid Q \rightarrow P' \mid Q$ if $P \rightarrow P'$	(PAR)
	$P' \rightarrow Q'$ if $P' \equiv P, P \rightarrow Q,$ and $Q \equiv Q'$	(EQUIV)

Fig. 4. The asynchronous  $\pi$ -calculus, its syntax and reduction semantics.

Processes of the form  $\bar{u}\tilde{v}$  are called *messages*;  $u$  is the target, whereas the sequence of names  $\tilde{v}$  represents the contents of the message. *Receptors* are processes of the form  $u(\tilde{x}).P$ , where  $u$  is called the location of the receptor,  $\tilde{x}$  the formal parameters, and  $P$  its body. The interaction between a message  $\bar{u}\tilde{v}$  and a receptor  $u(\tilde{x}).P$  is the process  $P$  where names in  $\tilde{v}$  replace those in  $\tilde{x}$ . The *parallel composition* of  $P$  and  $Q$  is written  $P \mid Q$ . Processes of the form  $\nu x P$  introduce a new name  $x$  local, or private, to  $P$ . A *replicated receptor*  $!u(\tilde{x}).P$  behaves as a persistent receptor surviving interaction with messages. Finally,  $\mathbf{0}$  represents the terminated process.

We say that a process  $P$  *occurs under a prefix* when  $P$  is in the scope of a  $u(\tilde{x})$ . We say that a name  $x$  occurs *free* in a process  $P$  if  $x$  is not in the scope of a  $\nu x$  or a  $u(\tilde{y}\tilde{x}\tilde{z})$ ;  $x$  occurs *bound* otherwise. The set  $\text{fn}(P)$  of the *free names* in  $P$ ; the result of simultaneously substituting  $\tilde{v}$  for the free occurrences of  $\tilde{u}$  in  $P$ , denoted by  $P\{\tilde{v}/\tilde{u}\}$ ; and *alpha equivalence*, denoted by  $\equiv_{\alpha}$ , are all defined in the standard way.

We follow the convention that, if  $P_1, \dots, P_n$  occur in a certain mathematical context, then in these processes all bound names are chosen to be different from the free names (cf. the variable convention (Barendregt, 1974)), except when otherwise mentioned.<sup>1</sup>

Processes of the form  $\nu x_1 \dots \nu x_n P$  are sometimes abbreviated to  $\nu x_1 \dots x_n P$ . As in the  $\lambda$ -calculus, where the  $\lambda$  binds looser than application, we take the view that  $\nu$  binds looser than the parallel composition:  $\nu x P \mid Q$  means  $\nu x (P \mid Q)$ . This saves us a few parenthesis when writing complex process terms.

The operational semantics of processes is given by a reduction relation. Following Milner (Milner, 1992), reduction exploits the *structural congruence* relation over process, written  $\equiv$ , and defined as the least congruence which is closed under the rules in figure 4. The *reduction relation* over process, written  $\rightarrow$ , is the smallest relation satisfying the rules in the same figure.

<sup>1</sup> A notable exception is the Replication Theorem 15.3.

States: typedprocess

typedprocess  $\stackrel{\text{def}}{=} \{P \in \text{process} \mid \Gamma \vdash P, \text{ for some } \Gamma\}$

Reduction:  $\rightarrow_d$

$P \rightarrow_d P' \stackrel{\text{def}}{=} (P \rightarrow P', P \rightarrow P'' \text{ implies } P' \equiv P'')$ , and  $P \not\downarrow_{\bar{u}}$

Equivalence:  $\simeq$

$P \simeq Q \stackrel{\text{def}}{=} C[P] \sim C[Q]$  for every  $C \in \text{typedcontext}$ , where  $\sim$  is the largest symmetric relation such that, whenever  $P \sim Q$  :

- i.  $P \downarrow_{\bar{u}}$  implies  $Q \downarrow_{\bar{u}}$ , and
- ii.  $P \rightarrow_d P'$  implies  $Q \rightarrow_d \sim P'$ .

---

Fig. 5. The Process machine.

The processes we are interested in obey the input/output type discipline. The set of input/output types is inductively defined as follows.

$$S ::= I\langle\tilde{S}\rangle \mid t \mid \mu t.S$$

$$I ::= o \mid i \mid b$$

Input/output types distinguish between the capability of *reading* a name ( $i\langle\tilde{S}\rangle$  is the type of a name that may be used only for input, and that carries names of types  $\tilde{S}$ ), *writing* on a name ( $o\langle\tilde{S}\rangle$  is the type of a name that may be used only for output), and *reading and writing* on a name ( $b\langle\tilde{S}\rangle$ ).

*Type environments*, or *typings*, denoted by  $\Gamma, \Delta$ , are finite maps from names to types. A *typing judgment*  $\Gamma \vdash P$  asserts that process  $P$  is *well typed* under the type environment  $\Gamma$ , or that  $P$  *obeys*  $\Gamma$ . We omit the definition of the  $\vdash$  relation, which can be found in book (Sangiorgi & Walker, 2001). If  $X$  is the set  $\{x_1, \dots, x_n\}$ , we write  $X : S$  to mean the typing  $x_1 : S, \dots, x_n : S$ .

### 3.5 The $\pi$ -process machine

States of the Process machine (figure 5) are processes typable with the input/output type assignment system.

Process reduction is nondeterministic. To conform to the requirement for reduction machines (Definition 1), we use *deterministic reduction*, a notion based on structural congruence and on an observation predicate. For any name  $u$ , the *observation predicate*  $\downarrow_{\bar{u}}$  denotes the possibility of a process immediately performing an output communication with the external environment along  $u$ . Thus,  $P \downarrow_{\bar{u}}$  holds if  $P$  has a sub-term  $\bar{u}v$  which is not underneath a prefix or in the scope of a restriction on  $u$ . We write  $P \not\downarrow_{\bar{u}}$  to mean that  $P \downarrow_{\bar{u}}$  does not hold for any  $u$ . Deterministic reduction is defined in figure 5.

A *context* is a process expression where the hole replaces an occurrence of  $\mathbf{0}$ . Contexts are denoted by  $C$ ; if  $C$  is a context and  $P$  a process, we denote by  $C[P]$  the process obtained by replacing  $P$  for the hole in  $C$ . Similarly to processes, we require contexts to be typed, as defined in figure 6. The equivalence for the Process machine is strong barbed congruence over typed processes, defined in figure 5.

$$\begin{array}{l}
\text{States: typedcontext} \\
\text{typedcontext} \stackrel{\text{def}}{=} \{C \mid C[P] \in \text{typedprocess, for all } P \in \text{typedprocess}\} \\
\text{Reduction: } \rightarrow_d \\
C \rightarrow_d C' \stackrel{\text{def}}{=} C[P] \rightarrow_d C'[P] \text{ for all } P \text{ s.t. } \text{fn}(P) \cap \text{variable} = \emptyset \\
\text{Equivalence: } \simeq \\
C \simeq C' \stackrel{\text{def}}{=} C[P] \simeq C'[P] \text{ for all } P \text{ s.t. } \text{fn}(P) \cap \text{variable} = \emptyset
\end{array}$$


---

Fig. 6. The Context machine.

**Proposition 14**

The triple  $\langle \text{typedprocess}, \rightarrow_d, \simeq \rangle$  is a reduction machine.

*Proof*

For the first clause in Definition 1, we know that  $\equiv \subseteq \simeq$  ((Sangiorgi & Walker, 2001), exercise 2.1.10). For the second, we show that  $\rightarrow_d$  commutes with  $\simeq$ , by recalling that strong barbed congruence coincides with strong barbed bisimulation in the asynchronous setting ((Sangiorgi & Walker, 2001), Theorem 5.4.10), and that the commutation is part of the definition of the latter.  $\square$

In proofs, as in examples, we sometimes use a stronger equivalence relation, contained in  $\simeq$ . The *garbage collection* relation is the equivalence that includes structural congruence and the following equality.

$$(vu \ !u(\tilde{x}).P) \mid Q \sim_{gc} Q.$$

At the basis of the proofs of the correspondences from CAM and SECD into the  $\pi$ -calculus lie the (sharpened) replication theorems. We say that  $u$ , a name free in  $P$ , is used as a trigger, if  $P$  obeys a typing  $\Gamma$ , and the tag of the type of  $u$  in  $\Gamma$  is  $o$ .

*Theorem 15 (Replication theorems, (Sangiorgi & Walker, 2001) 10.5.1(2) and (1), 2.2.28(4))*

If  $u$  is used as a trigger in  $P, Q, R$ , then

1.  $vu \ !u(\tilde{x}).P \mid v(\tilde{y}).Q \simeq v(\tilde{y}).(vu \ !u(\tilde{x}).P \mid Q)$  for  $u \neq v$ ;
2.  $vu \ !u(\tilde{x}).P \mid !v(\tilde{y}).Q \simeq !v(\tilde{y}).(vu \ !u(\tilde{x}).P \mid Q)$ ;
3.  $vu \ !u(\tilde{x}).P \mid Q \mid R \simeq (vu \ !u(\tilde{x}).P \mid Q) \mid (vu \ !u(\tilde{x}).P \mid R)$ .

**3.6 The  $\pi$ -context machine**

The notions of reduction and equivalence for typed contexts are derived from those of typed process, by taking the view that the process in the hole must not play any rôle; their definitions are in figure 6. The symbols for reduction and equivalence are those of the process machine; in each particular case, it should be clear which definition to use.

**Proposition 16**

The triple  $\langle \text{typedcontext}, \rightarrow_d, \simeq \rangle$  is a reduction machine.

*Proof*

Follows from the fact that  $\langle \text{typedprocess}, \rightarrow_d, \simeq \rangle$  is a reduction machine.  $\square$

$$\begin{aligned} \text{load} &: \text{term}^0 \rightarrow \text{cam} \\ \text{load}(M) &\stackrel{\text{def}}{=} \langle M[], \text{nil} \rangle \end{aligned}$$

Fig. 7. The Lambda to CAM correspondence.

#### 4 Four correspondences

This section presents three operational correspondences (Lambda into CAM, CAM into Process, and SECD into Context), and one convergence correspondence (Lambda into SECD). It concludes by deriving direct translations from Lambda into Process and into Context.

##### 4.1 The Lambda to CAM correspondence

The call-by-value lambda machine and the CAM machine are defined in figures 1 and 2. The Lambda to CAM map is defined in figure 7. Since alpha-equivalent terms are loaded into CAM-equivalent states, load is a correspondence.

*Lemma 17*

$M \rightarrow_v N$  implies  $\langle M[], s \rangle \rightarrow_k^* \sim_k \langle N[], s \rangle$ .

*Proof*

By induction on the derivation of the lambda reduction step.

**Case** the last rule is  $\beta$ : apply reduction rules APP, EXCH, and CALL, followed by the last rule in the equivalence.

$$\begin{aligned} \langle ((\lambda x M)V) [], s \rangle &\rightarrow_k \langle (\lambda x M) [], r : V [] : s \rangle \rightarrow_k \langle V [], l : (\lambda x M) [] : s \rangle \rightarrow_k \\ &\langle M[x := V []], s \rangle \sim_k \langle (M\{V/x\}) [], s \rangle \end{aligned}$$

**Case** the last rule is  $v$ : apply reduction rules APP, EXCH, followed by induction, and by the first rule in equivalence.

$$\begin{aligned} \langle (VM) [], s \rangle &\rightarrow_k \langle V [], r : M [] : s \rangle \rightarrow_k \langle M [], l : V [] : s \rangle \rightarrow_k^* \sim_k \\ &\langle M' [], l : V [] : s \rangle \sim_k \langle (VM') [], s \rangle \end{aligned}$$

**Case** the last rule is  $\mu$ : apply reduction rule APP, followed by induction; conclude with the second rule in equivalence.

$$\langle (MN) [], s \rangle \rightarrow_k \langle M [], r : N [] : s \rangle \rightarrow_k^* \sim_k \langle M' [], r : N [] : s \rangle \sim_k \langle (M'N) [], s \rangle$$

□

*Theorem 18*

The load correspondence is operational.

*Proof*

We show that the three clauses of definition 2.2 hold. (a) A corollary of lemma 17. (b) We show the contrapositive, namely, that if  $M$  diverges, then so does  $\text{load}(M)$ . If  $M$  diverges, then rule  $\beta$  is used an unbounded number of times. By analysing the first case in the proof of Lemma 17, one concludes that rule CALL is applied as many times. (c) We again show the contrapositive, namely,  $M \not\rightarrow_v$  implies  $\text{load}(M) \not\rightarrow_k$ . If  $M \not\rightarrow_v$ , then  $M$  is a closed value, say  $V$ , and  $\langle V [], \text{nil} \rangle \not\rightarrow_k$ . □

$$\begin{aligned} \text{load} &: \text{term}^0 \rightarrow \text{dump} \\ \text{load}(M) &\stackrel{\text{def}}{=} \langle \text{nil}, \emptyset, M, \text{nil} \rangle \end{aligned}$$

---

Fig. 8. The Lambda to SECD correspondence.

Theorem 5.1 allows us to conclude that  $\text{load}$  is convergent as well.

*Corollary 19*

The  $\text{load}$  correspondence is convergent.

#### 4.2 The Lambda to SECD correspondence

Recall the call-by-value lambda machine and the SECD machine from figures 1 and 3. The Lambda to SECD map is defined in figure 8. Since alpha-equivalent terms are loaded into SECD-equivalent dumps,  $\text{load}$  is a correspondence.

*Theorem 20 (Plotkin (Plotkin, 1975), theorem 1)*

The  $\text{load}$  correspondence is convergent.

The only point worth notice is that Plotkin shows that if  $M \downarrow_v M'$ , then  $\text{load}(M) \downarrow_s \langle vc, \emptyset, \text{nil}, \text{nil} \rangle$  where  $\text{real}(vc) = M'$ . We then apply the dump equivalence to obtain  $\text{load}(M) \downarrow_s \sim_s \text{load}(M')$ , as required.

#### 4.3 The CAM to Process correspondence

The CAM machine and the  $\pi$ -process machine are the objects of Definitions 2 and 5. Based on a variant of Milner's encoding, this section presents a translation of CAM states into  $\pi$ -processes and proves that it constitutes an operational correspondence.

The encoding is described in figure 9. An *environment entry*  $u := (\lambda xM)[e]$  describes the encoding of an abstraction  $\lambda xM$  with environment  $e$  located at name  $u$ ; it gets at  $u$  the argument  $x$  to the function as well as the channel  $p$  where to locate the result; if the evaluation of the function converges, then this channel is written with the location of the result. Replication allows the entry to be used multiple times. Values write the name they are know by (either the variable  $x$  itself, or the location of the environment entry  $u$  representing the abstraction) on the reply-to name  $p$  that locates the value. In order to mimic an application  $MN$ , we consider three cases. When  $M$  is an application, we first evaluate the function  $M$  on some fresh reply-to name  $q$ , and wait for the result  $u$  on  $q$ . Then we do the same for the argument  $N$ , thus obtaining its value  $v$ . All that remains is to invoke  $u$  with  $v$ , requesting for the resulting value to be sent on  $p$ , the location of application  $MN$ . The cases for applications  $xM$  and  $(\lambda xM)N$  are obtained from the above encoding by partial evaluation. One can easily see that, for example,  $vq \llbracket x \rrbracket_q \mid q(u).(vr \llbracket M \rrbracket_r \mid r(v).\bar{u}vp)$  reduces in one deterministic step to  $vr \llbracket M \rrbracket_r \mid r(v).\bar{x}vp$ ; so we set the latter to be the image of  $\llbracket xM \rrbracket_p$ , rather than the general case as in Milner's encoding.



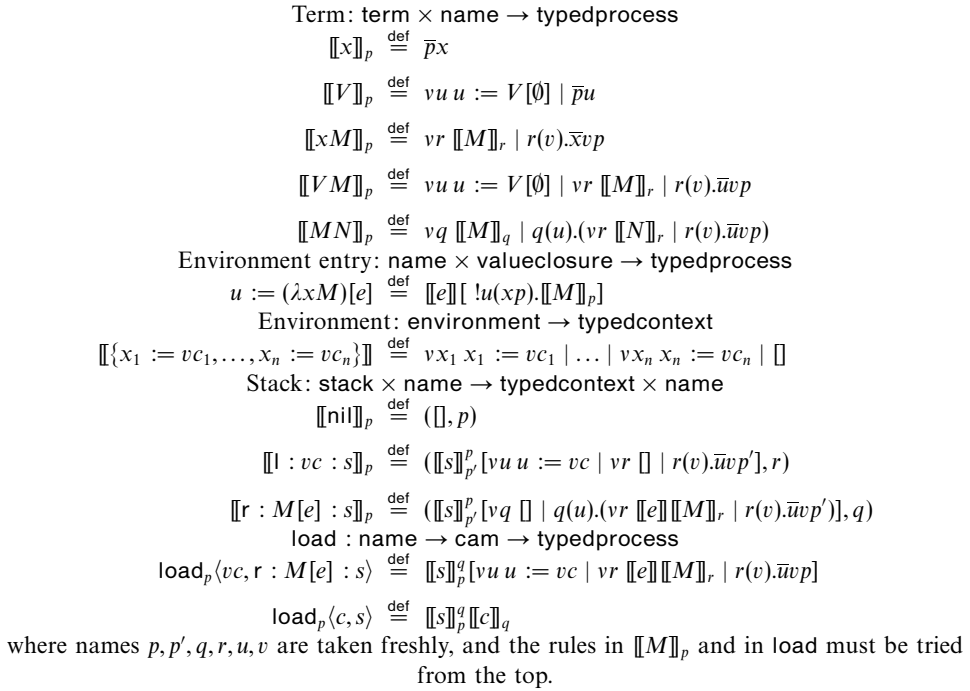


Fig. 9. The CAM to Process correspondence.

An *environment*  $e$  is encoded by translating each of its entries (recursively for the environments in the entries); the locations of the entries (the various  $x_i$ ) are then made local to a hole where we place the encoding of a term  $M$ , thus providing for the encoding of a *closure*  $M[e]$ .

A *stack*  $s$  is compiled by orderly unstacking its elements. The result of encoding a stack is a pair composed of a context and a name: the hole should be filled with (the translation of) a term located at the name. If  $\llbracket s \rrbracket_p = (C, q)$ , we write  $\llbracket s \rrbracket_p^q$  to denote the context  $C$ , and use name  $q$  wherever needed. So, for example,

$$\llbracket s \rrbracket_p^q \llbracket c \rrbracket_q \text{ means ' } C[\llbracket c \rrbracket_q] \text{ where } \llbracket s \rrbracket_p = (C, q) \text{'}$$

The idea is that *subscripts represent input* to the encoding function, whereas *superscripts represent output*. The translation of states should be easy to understand by comparing the contexts in the hole of  $\llbracket s \rrbracket_p$  with the encodings for  $VM$ , and  $MN$ , respectively. Notice that the hole in a context  $\llbracket s \rrbracket_p$  is under no prefix.

Finally, to compile a *state*  $\langle c, s \rangle$ , we compile the stack  $s$  into a context (whose hole is to be located at  $q$ ), and fill the hole with the encoding of closure  $c$  compiled at  $q$ . Special care must be taken when the head of the stack is an  $r$  and the closure in the state is a value closure, for the closure represents the argument to the function that follows the  $r$ ; the encoding of this case should be easy to understand by referring to the encoding for  $VM$  and rule EXCH in reduction (figure 2).

We argue that the encoding yields typed processes. There are two kinds of names involved:

1. *value names* comprising variables  $x, y, z$ , as well as locations of abstractions  $u, v, w$ , all in *variable*; and
2. *reply-to names*  $p, q, r$ , names that return value names.

Let us call  $\text{Val}$  the type of the value names. Analysing the message  $\bar{u}vp$  in the encoding of an application, we see that  $\text{Val}$  must be of the form  $\text{oVal}, T$ , for  $T$  the type of the reply-to name  $p$ . Then, concentrating on the message  $\bar{p}x$  in the encoding of a variable, we easily conclude that  $T$  is the type  $\text{oVal}$ . Type  $\text{Val}$  is then  $\mu X.\text{o}\langle X, \text{o}X \rangle$ . For terms, we know that ((Sangiorgi & Walker, 2001), lemma 15.3.14),

$$\text{fv}(M) : \text{Val}, p : \text{oVal} \vdash \llbracket M \rrbracket_p.$$

For states, we can equally show that  $p : \text{oVal} \vdash \text{load}(s)_p$ , since states are closed (that is, for each  $M[e]$  in a state, we have that  $\text{fv}(M) \subseteq \text{dom}(e)$ , cf. figure 2).

In the rest of this section we show that  $\text{load}$  is an operational correspondence. Let  $u := V$  abbreviate  $u := V[\emptyset]$ ; we start with a basic result that helps in establishing that  $\text{load}$  is a correspondence.

*Lemma 21 (Substitution Lemma)*

$$\llbracket x := V \rrbracket_p \llbracket M \rrbracket_p \simeq \llbracket M\{V/x\} \rrbracket_p.$$

*Proof*

By structural induction on  $M$ .

**Case**  $M$  is  $x$ . We have  $vxx := V \mid \bar{p}x \stackrel{\text{def}}{=} \llbracket V \rrbracket_p \stackrel{\text{def}}{=} \llbracket x\{V/x\} \rrbracket_p$ .

**Case**  $M$  is  $y \neq x$ . We have  $vxx := V \mid \bar{p}y \sim_{\text{gc}} \bar{p}y \stackrel{\text{def}}{=} \llbracket y \rrbracket_y \stackrel{\text{def}}{=} \llbracket y\{V/y\} \rrbracket_p$ .

**Case**  $M$  is  $\lambda yN$ . Using Replication Theorem 15.2, and induction, we have

$$\begin{aligned} vxx := V \mid vuu := \lambda yN \mid \bar{p}u &\simeq vu \text{ !}u(yq).(vxx := V \mid \llbracket N \rrbracket_q) \mid \bar{p}u \simeq \\ &vu \text{ !}u(yq).\llbracket N\{V/x\} \rrbracket_q \mid \bar{p}u \stackrel{\text{def}}{=} \llbracket (\lambda yN)\{V/x\} \rrbracket_p. \end{aligned}$$

**Case**  $M$  is  $NL$ ,  $N \notin \text{value}$ . Using Replication Theorem 15.3, and induction, we have

$$\begin{aligned} vxx := V \mid vq \llbracket N \rrbracket_q \mid q(u).(vr \llbracket L \rrbracket_r \mid r(v).\bar{u}vp) &\simeq \\ vxx := V \mid vq \llbracket N \rrbracket_q \mid q(u).(vryy := V \mid \llbracket L\{y/x\} \rrbracket_r \mid r(v).\bar{u}vp) &\simeq \\ vv \ q \llbracket N\{V/x\} \rrbracket_q \mid q(u).(vr \llbracket L\{V/x\} \rrbracket_r \mid r(v).\bar{u}vp) &\stackrel{\text{def}}{=} \\ &\llbracket N\{V/x\}L\{V/x\} \rrbracket_p. \end{aligned}$$

**Case**  $M$  is  $xL$ , or  $M$  is  $(\lambda yN)L$ : similar.  $\square$

*Proposition 22*

The  $\text{load}_p$  map is a correspondence.

*Proof*

For the first two equations in the definition of  $\sim_k$ , one can easily see that the encoding of the left-hand side is structural congruent to that of the right-hand side. For the last equation, use the Substitution Lemma 21.  $\square$

Table 1. The relation that holds between  $\text{load}_p(k)$  and  $\text{load}_p(k')$  when  $k \rightarrow_k k'$

CAM reduction rule	Relation on processes
VAR	$\sim_{\text{gc}}$
APP	$\simeq$
EXCH	$=$
CALL	$\rightarrow_d^2 \sim_{\text{gc}}$

The following result is used to set that load is operational.

*Theorem 23*

$k \rightarrow_k k'$  implies  $\text{load}_p(k) \rightarrow_d^* \simeq \text{load}_p(k')$ , for all  $p$ .

*Proof*

A case analysis on the reduction rules. The relation that holds between  $\text{load}_p(k)$  and  $\text{load}_p(k')$  is summarised in Table 1.

**Case VAR:** directly from the definition, collecting as garbage those entries in  $\llbracket e \rrbracket$  but not in  $\llbracket e(x) \rrbracket$ .

**Case APP:** When  $M$  is an application, we have

$$\begin{aligned} \text{load}_p\langle (MN)[e], s \rangle &\stackrel{\text{def}}{=} \llbracket s \rrbracket_p^q \llbracket e \rrbracket [vq \llbracket M \rrbracket_q \mid q(u).(vr \llbracket N \rrbracket_r \mid r(v).\bar{u}vp)] \\ \text{load}_p\langle M[e], r : N[e] : s \rangle &\stackrel{\text{def}}{=} \llbracket s \rrbracket_p^q [vq \llbracket e \rrbracket \llbracket M \rrbracket_q \mid q(u).(vr \llbracket e \rrbracket \llbracket N \rrbracket_r \mid r(v).\bar{u}vp)] \end{aligned}$$

Start from the right-hand side of the second equation. Use replication theorem 15.1 as many times as there are entries in  $e$ , to bring the second  $\llbracket e \rrbracket$  outside the scope of  $q(u)$ . Use then replication theorem 15.3 the same number of times to “merge” the two environments, thus obtaining the right-hand side of the first equation. Proceed similarly when  $M$  is a variable or an abstraction.

**Case EXCH:** directly from the definition.

**Case CALL:**

$$\begin{aligned} \text{load}_p\langle V[e'], l : (\lambda xM)[e] : s \rangle &\stackrel{\text{def}}{=} \\ \llbracket s \rrbracket_p^p [vu \llbracket e \rrbracket [u := \lambda xM \mid vr \llbracket V[e'] \rrbracket_r \mid r(v).\bar{u}vp'] &\rightarrow_d \\ \llbracket s \rrbracket_p^p [vu \llbracket e \rrbracket [u := \lambda xM \mid vx \llbracket e' \rrbracket [x := V \mid \bar{u}xp'] &\rightarrow_d \\ \llbracket s \rrbracket_p^p [vu \llbracket e \rrbracket [u := \lambda xM \mid vx \llbracket e' \rrbracket [x := V \mid \llbracket M \rrbracket_p'] &\sim_{\text{gc}} \\ \text{load}_p\langle M[e\{x := V[e']\}], s \rangle & \end{aligned}$$

□

*Theorem 24*

The  $\text{load}_p$  correspondence is operational.

*Proof*

We show that the three clauses of definition 2.2 hold.

(a) Theorem 23. (b) We show its contrapositive, namely that, if  $k$  diverges then so does  $\text{load}_p(k)$ . After inspecting the table in the proof of theorem 23, we conclude that it suffices to show that if  $k$  diverges, then rule CALL is applied an unbounded

$$\begin{array}{l}
\text{Term: } \text{term} \rightarrow \text{typedcontext} \times \text{name} \\
\llbracket x \rrbracket \stackrel{\text{def}}{=} (\square, x) \\
\llbracket \lambda x.M \rrbracket \stackrel{\text{def}}{=} (v u u := \lambda x.M \mid \square, u) \\
\llbracket MN \rrbracket \stackrel{\text{def}}{=} (vr \llbracket M \rrbracket^v \llbracket N \rrbracket^w \bar{v} r \mid r(u). \square, u) \\
\text{Environment entry: } \text{name} \times \text{valueclosure} \rightarrow \text{typedprocess} \\
u := (\lambda x.M)[e] \stackrel{\text{def}}{=} \llbracket e \rrbracket [!u(xr). \llbracket M \rrbracket^v \bar{r} v] \\
\text{Environment: } \text{environment} \rightarrow \text{typedcontext} \\
\llbracket \{x_1 := vc_1, \dots, x_n := vc_n\} \rrbracket \stackrel{\text{def}}{=} vx_1 x_1 := vc_1 \mid \dots \mid vx_n x_n := vc_n \mid \square \\
\text{Stack: } \text{stack} \rightarrow \text{typedcontext} \times \text{name}^+ \\
\llbracket vc_1 : \dots : vc_n \rrbracket \stackrel{\text{def}}{=} (\llbracket \{x_1 := vc_1, \dots, x_n := vc_n\} \rrbracket, x_1 \dots x_n) \\
\text{load: } \text{name}^+ \times \text{dump} \rightarrow \text{typedcontext} \times \text{name} \\
\text{load}_{\bar{u}} \langle v c : \_ \_ \_ , \text{nil}, \langle s, e, C, D \rangle \rangle \stackrel{\text{def}}{=} (\llbracket s : vc \rrbracket^{\bar{v}} [\text{load}_{\bar{u}v}^w \langle \text{nil}, e, C, D \rangle], w) \quad (\text{RET}) \\
\text{load}_{\bar{u}} \langle s, e, C, D \rangle \stackrel{\text{def}}{=} (\llbracket s \rrbracket^{\bar{v}} [\text{load}_{\bar{u}v}^w \langle \text{nil}, e, C, D \rangle], w) \quad (\text{STK}) \\
\text{load}_{\bar{u}} \langle \text{nil}, e', M, \langle s, e, C, D \rangle \rangle \stackrel{\text{def}}{=} (vr \llbracket e' \rrbracket \llbracket M \rrbracket^{w'} \bar{r} w' \mid \\
\quad r(w). \llbracket s \rrbracket^{\bar{v}} \text{load}_{\bar{u}w}^w \langle \text{nil}, e, C, D \rangle, w') \quad (\text{TERM}) \\
\text{load}_{\bar{u}} \langle \text{nil}, e, M : C, D \rangle \stackrel{\text{def}}{=} (\llbracket e \rrbracket \llbracket M \rrbracket^v [\text{load}_{\bar{u}v}^w \langle \text{nil}, e, C, D \rangle], w) \quad (\text{CTR}) \\
\text{load}_{\bar{u}w} \langle \text{nil}, e, \text{ap} : C, D \rangle \stackrel{\text{def}}{=} (vr \bar{w} vr \mid r(u'). \text{load}_{\bar{u}u'}^e \langle \text{nil}, e, C, D \rangle, v') \quad (\text{CALL}) \\
\text{load}_{\bar{u}v} \langle D \rangle \stackrel{\text{def}}{=} (\square, v) \quad (\text{DONE})
\end{array}$$

where names  $r, x_1, \dots, x_n, u, u', v, w, w'$  are taken freshly, and the rules in load must be tried from the top.

Fig. 10. The SECD to Context correspondence.

number of times. This must true since the CAM machine without rule CALL converges (lemma 10). (c) We again show the contrapositive, namely that  $k \not\rightarrow_k$  implies  $\text{load}(k) \not\rightarrow_d$ . Terminal states are of the form  $\langle vc, \text{nil} \rangle$ . Clearly,  $\text{load}_p \langle vc, \text{nil} \rangle \not\rightarrow_d$ .  $\square$

Theorem 5.1 allows us to conclude that load is convergent as well.

*Corollary 25*

The  $\text{load}_p$  correspondence is convergent.

#### 4.4 The SECD to Context correspondence

Recall the SECD machine and the  $\pi$ -context machine from figures 3 and 6. This section presents an encoding of SECD states (that is, dumps) into  $\pi$ -contexts and prove that it constitutes an operational correspondence.

The encoding is described in figure 10 and is based on that in Vasconcelos (1994). The encoding of a *term*  $M$  is a pair composed of a context and a name: the name represents the location of the encoding of  $M$ , as seen by whatever process we place in the hole. So we see that  $\llbracket M \rrbracket$  chooses the name where to locate  $M$ ; whereas, in the encoding of the previous section, the name where to locate  $M$  is an argument to the encoding function. Another difference is that the result of the encoding is a

context (together with a name), whereas that of Milner is a process. In contrast with the encoding of the previous section, values do not write their locations in some reply-to name. Instead they “let” the hole directly know its location: the location of a variable  $x$  is simply  $x$ ; that of an abstraction is a new name  $u$  in whose scope we place both the hole and the corresponding environment entry. Applications  $MN$  first evaluate  $M$  to  $v$ , then  $N$  to  $w$ , and then invoke the function at  $v$  with the argument  $w$  and request to have the result sent to a newly created name  $r$ . Finally, they wait for the result at  $r$ , and instantiate it in the hole as  $u$ , since the process in the hole expects to see the value of  $MN$  located at  $u$ .

Similarly to the encoding of stacks in figure 9,  $\llbracket M \rrbracket^v$  denotes the first component of the pair  $(C, v)$  obtained by running function  $\llbracket \cdot \rrbracket$  on  $M$ , so that, in a more conventional, albeit less concise, form, we could have written:

$$\llbracket MN \rrbracket \stackrel{\text{def}}{=} \nu r C[C'[\bar{v}wr] \mid r(u).\square] \text{ where } \llbracket M \rrbracket \stackrel{\text{def}}{=} (C, v) \text{ and } \llbracket N \rrbracket \stackrel{\text{def}}{=} (C', w).$$

When  $v$  is not important we write  $\llbracket M \rrbracket$  to denote the first component (the context) of the pair.

*Environments, environment entries, and closures* are compiled in exactly the same way as their CAM counterparts, except for the new definition of the encoding of terms. *Stacks* are, however, compiled quite differently. Essentially a stack is encoded into the parallel composition of (the encodings of) its elements: the order is kept by the list of names returned by the encoding function. Notice that, in  $\llbracket s \rrbracket = (C, \vec{w})$ , name  $v$  denotes the (location of the) top of the stack. Further notice that  $\text{name}^+$  in the signature of the mapping for stacks, makes load rule  $\text{STK}$  applicable only for non-empty stacks (thus avoiding an infinite recursion).

load proceeds by orderly compiling, first the  $\lambda$ -terms in the stack, and then those in the control string (rules  $\text{STK}$ , and  $\text{CTR}$ ). To keep hold of the location of these terms, our encoding works with a non-empty sequence of names ( $\text{name}^+$  in the signature of the encoding), rather than a single name. In this way, when time comes to compile a dump with an  $\text{ap}$  mark at the head of the control string, we know which function to call with which argument (rule  $\text{CALL}$ ). The  $\text{RET}$  reduction rule in figure 3 ignores the whole stack in the dump, but its top: before trying the general rule to compile the stack ( $\text{STK}$ ), we try the  $\text{RET}$  that orderly compiles the top of the stack  $vc$  as well as the stack  $s$  in the dump part of the state.

Using rules  $\text{CTR}$  and  $\text{DONE}$ , we can easily see that the dump  $\langle \text{nil}, \emptyset, M, \text{nil} \rangle$  (obtained by loading a closed term  $M$  into the SECD machine, cf. figure 8) is compiled into  $\llbracket M \rrbracket$ , as expected.

Recall that  $\llbracket M \rrbracket$  yields a pair composed of a context and a name; below we write  $\llbracket M \rrbracket$  to denote the context alone, and abbreviate  $\llbracket e \rrbracket \llbracket M \rrbracket$  to  $\llbracket M[e] \rrbracket$ . The rest of this section shows that load is an operational correspondence. We start with a result that helps in establishing that load is a correspondence.

*Lemma 26 (Substitution Lemma)*

$$\llbracket M[x := V] \rrbracket \simeq \llbracket M\{V/x\} \rrbracket.$$

*Proof*

By induction on the structure of  $M$ , using the replication theorems.

**Case  $M$**  is the variable  $x$ ; we have  $\llbracket x[x := V] \rrbracket \stackrel{\text{def}}{=} \llbracket V \rrbracket$ .

**Case  $M$**  is a variable  $y \neq x$ ; we have  $\llbracket y[x := V] \rrbracket \stackrel{\text{def}}{=} vx \ x := V \mid \square \simeq \square \stackrel{\text{def}}{=} \llbracket y \rrbracket$ , noticing that the two contexts are barbed congruent, for  $x$ , being a variable, cannot be free in the hole (cf. figure 6).

**Case  $M$**  is an abstraction  $\lambda y.N$ . Using the Replication theorem 15.2 (notice  $x$  being a variable is not free on the hole), and induction, we have

$$vx \ x := V \mid vv \ !v(yr).\llbracket N \rrbracket^u \bar{r}u \mid \square \simeq vv \ !v(yr).\llbracket L[x := V] \rrbracket^u \bar{r}v \mid \square \simeq \\ vv \ !v(yr).\llbracket L\{V/x\} \rrbracket^u \bar{r}v \mid \square \stackrel{\text{def}}{=} \llbracket (\lambda y.N)\{V/x\} \rrbracket$$

**Case  $M$**  is an application  $NL$ . Using the Replication Theorem 15.3, and induction twice, we have:

$$vx \ x := V \mid vr \ \llbracket N \rrbracket \llbracket L \rrbracket^w \bar{v}wr \mid r(u).\square \simeq \\ \llbracket N[x := V] \rrbracket \llbracket L[x := V] \rrbracket^w \bar{v}wr \mid r(u).\square \simeq \\ \llbracket N\{V/x\} \rrbracket \llbracket L\{V/x\} \rrbracket^w \bar{v}wr \mid r(u).\square \stackrel{\text{def}}{=} \\ \llbracket (NL)\{V/x\} \rrbracket$$

□

*Proposition 27*

The  $\text{load}_p$  map is a correspondence, for all  $p$ .

*Proof*

For the first equation in the definition of  $\sim_s$ , it suffices to show that

$$\llbracket vc \rrbracket \simeq \llbracket \text{real}(vc) \rrbracket.$$

Let  $vc = V[\{x_1 := vc_1, \dots, x_n := vc_n\}]$ , and  $\text{fv}(V) = \{x_1, \dots, x_k\}$ , for some  $k \leq n$ . We proceed by induction on the definition of  $\text{real}$ .

$$\llbracket \text{real}(vc) \rrbracket \stackrel{\text{def}}{=} \\ \llbracket V\{\text{real}(vc_1)/x_1\} \dots \{\text{real}(vc_k)/x_k\} \rrbracket \simeq \quad \text{(Substitution lemma 26)} \\ \llbracket V\{x_1 := \text{real}(vc_1), \dots, x_k := \text{real}(vc_k)\} \rrbracket \sim_{\text{gc}} \quad \text{(Garbage collection)} \\ \llbracket V\{x_1 := \text{real}(vc_1), \dots, x_n := \text{real}(vc_n)\} \rrbracket \simeq \quad \text{(Induction)} \\ \llbracket V\{x_1 := vc_1, \dots, x_n := vc_n\} \rrbracket \stackrel{\text{def}}{=} \\ \llbracket vc \rrbracket$$

For the second equation, we can easily check that  $\llbracket vc \rrbracket \equiv_x \llbracket vc' \rrbracket$ , when  $\text{real}(vc) \equiv_x \text{real}(vc')$ . □

The following result is used to set that load is operational.

*Theorem 28*

$D \rightarrow_s D'$  implies  $\text{load}_u(D) \rightarrow_d^* \simeq \text{load}_u(D')$ .

Table 2. The relation that holds between  $\text{load}_u(D)$  and  $\text{load}_u(D')$  when  $D \rightarrow_s D'$

SECD reduction rule	Relation on contexts
VAR	$\sim_{\text{gc}}$
ABS	$\equiv$
APP	$\simeq$
CALL	$\rightarrow_d \sim_{\text{gc}}$
RET	$=$

*Proof*

A case analysis on the reduction rules. The relation that holds between  $\text{load}_u(D)$  and  $\text{load}_u(D')$  is summarised in Table 2.

**Case VAR:** from  $\text{load}(D)$  to  $\text{load}(D')$ , use STK, CTR,  $\sim_{\text{gc}}$ , and STK. Notice that  $\llbracket e(x) \rrbracket \sim_{\text{gc}} \llbracket x[e] \rrbracket$ , where we have garbage collected all entries in  $e$  different from  $x$ .

**Case ABS:** use STK, CTR,  $\equiv$ , and STK, in this order.

**Case APP:** use STK, CTR,  $\simeq$ , CALL, CTR, CTR, and STK. For the  $\simeq$  step we show that  $\llbracket e \rrbracket \llbracket M \rrbracket^u \llbracket e \rrbracket \llbracket N \rrbracket^v \simeq \llbracket e \rrbracket \llbracket N \rrbracket^u \llbracket M \rrbracket^v$ . Applying the replication Theorem 15.3 as many times as there are entries in  $e$ , we have:

$$\llbracket e \rrbracket \llbracket M \rrbracket^u \llbracket e \rrbracket \llbracket N \rrbracket^v \equiv_x \llbracket e \rrbracket \llbracket M \rrbracket^u \llbracket e\sigma \rrbracket \llbracket M\sigma \rrbracket^v \equiv \llbracket e \rrbracket \llbracket e\sigma \rrbracket \llbracket M \rrbracket^u \llbracket M\sigma \rrbracket^v \simeq \llbracket e \rrbracket \llbracket M \rrbracket^u \llbracket M \rrbracket^v$$

where  $\sigma$  is an injective substitution with the domain of  $e$ , and whose codomain is fresh.

**Case CALL:** use STK, CALL,  $\rightarrow_d$ ,  $\sim_{\text{gc}}$ ,  $\equiv$  and TERM.

$$\begin{aligned} & \text{load}_{\bar{u}} \langle (\lambda x M)[e'] : vc : s, e, \text{ap} : C, D \rangle \stackrel{\text{def}}{=} \\ & \llbracket s \rrbracket^{\bar{v}} \llbracket vc \rrbracket^{w_1} \llbracket (\lambda x M)[e'] \rrbracket^{w_2} [vr \bar{w}_2 w_1 r \mid r(w). \text{load}_{\bar{u}\bar{v}w}^v \langle \text{nil}, e, C, D \rangle] \rightarrow_d \sim_{\text{gc}} \\ & \llbracket s \rrbracket^{\bar{v}} \llbracket vc \rrbracket^{w_1} [vr \llbracket M\{w_1/x\}[e'] \rrbracket^{w'} \bar{r}w' \mid r(w). \text{load}_{\bar{u}\bar{v}w}^v \langle \text{nil}, e, C, D \rangle] \equiv \\ & \llbracket s \rrbracket^{\bar{v}} [vr \llbracket M[e'\{x := vc\}] \rrbracket^{w'} \bar{r}w' \mid r(w). \text{load}_{\bar{u}\bar{v}w}^v \langle \text{nil}, e, C, D \rangle] \stackrel{\text{def}}{=} \\ & \text{load}_{\bar{u}} \langle \text{nil}, e'\{x := vc\}, M, \langle s, e, C, D \rangle \rangle \end{aligned}$$

For the  $\simeq$  step we collect the  $w_2$  environment entry. For the  $\equiv$  step, it follows from the encoding that the context  $\llbracket vc \rrbracket^{w_1} \llbracket M\{w_1/x\}[e'] \rrbracket^{w'}$  is structural congruent to the context  $\llbracket M[e'\{x := vc\}] \rrbracket^{w'}$ .

**Case RET:** use RET followed by STK.  $\square$

*Theorem 29*

The  $\text{load}_u$  correspondence is operational, for all  $u$ .

*Proof*

We show that the three clauses of definition 2.2 hold.

(a) Theorem 28. (b) We show its contrapositive, namely, if  $D$  diverges then so does  $\text{load}_u(D)$ . Inspecting the table in the proof of theorem 28, we conclude that it suffices to show that if  $M$  diverges, then rule CALL is applied an unbounded number of times.

This must be true since the SECD machine without the CALL converges (lemma 13). (c) We again show the contrapositive, namely that  $D \not\rightarrow_s$  implies  $\text{load}_u D \not\rightarrow$ , by analyzing the dumps that do not reduce. They are of the form  $\langle vc, \rightarrow, \text{ap} : \rightarrow, \_ \rangle$ ,  $\langle \text{nil}, \rightarrow, \text{ap} : \rightarrow, \_ \rangle$ ,  $\langle \text{nil}, \_ , \text{nil}, \_ \rangle$ ,  $\langle \_ , \_ , \text{nil}, \text{nil} \rangle$ , and  $\text{nil}$ . Clearly their encodings do not reduce.  $\square$

Theorem 5.1 allows us to conclude that  $\text{load}$  is convergent as well.

*Corollary 30*

The  $\text{load}_u$  correspondence is convergent.

#### 4.5 Lambda to Pi, directly

We can easily see that the Lambda to Process correspondence is operational. In fact, we know that both the Lambda to CAM and the CAM to Process correspondences are operational, and that the composition of two operational correspondences is operational (Theorems 18, 24, and 5.2).

Similarly, the Lambda into Context correspondence is convergent. This follows from the following results: the Lambda to SECD correspondence is convergent, the SECD to Context is operational, the composition of a convergent with an operational correspondence is convergent (Theorems 20, 29, and 5.4).

Finally, given that operational correspondences are convergent (Theorem 5.1), we conclude that the Lambda to Process correspondence is convergent.

*Theorem 31*

1. The Lambda to Process correspondence is operational;
2. The Lambda to Context correspondence is convergent.
3. The Lambda to Process correspondence is convergent.

The remainder of this section studies direct encodings of the  $\lambda$ -calculus into the  $\pi$ -calculus, establishing the adequacy of the two encodings. The task is simplified since, in both cases, we proved the respective Substitution Lemmas (21, 26). The direct proofs below also allow us to quantify the number of  $\pi$ -steps involved in the simulation of a  $\lambda$ -step: exactly two in each case (on average for the Context case). We start with the Process case.

*Theorem 32*

Let  $\llbracket \cdot \rrbracket$  be the map defined in figure 9.  $M \rightarrow_v N$  implies  $\llbracket M \rrbracket \rightarrow_d^2 \simeq \llbracket N \rrbracket$ .

*Proof*

By transition induction.

**Case** the last rule is  $\beta$  and  $M$  is  $(\lambda x M)y$ .

$$\llbracket (\lambda x M)y \rrbracket_p \rightarrow_d^2 (vu u := \lambda x M) \mid \llbracket M\{y/x\} \rrbracket \sim_{\text{gc}} \llbracket M\{y/x\} \rrbracket.$$

**Case** the last rule is  $\beta$  and  $M$  is  $(\lambda x M)V$ , for  $V$  an abstraction. Using the Substitution Lemma 21, we have:

$$\llbracket (\lambda x M)V \rrbracket_p \rightarrow_d^2 (vu u := \lambda x M) \mid vv v := V \mid \llbracket M\{v/x\} \rrbracket_p \sim_{\text{gc}} \llbracket M\{V/x\} \rrbracket_p$$



**Case** the last rule is  $v$ . Directly by induction, both for  $V$  a variable and an abstraction.

**Case** the last rule is  $\mu$ . Directly by induction.  $\square$

The above result should be contrasted with the one obtained by Sangiorgi and Walker for the unoptimized version of the translation from terms to processes. When one omits the particular cases of the translation of applications ( $\llbracket xM \rrbracket_p$  and  $\llbracket VM \rrbracket_p$  in figure 9) all we can say about processes  $\llbracket M \rrbracket_p$  and  $\llbracket N \rrbracket_p$  is that  $\llbracket M \rrbracket_p \rightarrow_d^{n+3} \simeq P$ , and  $\llbracket N \rrbracket_p \rightarrow_d^n P$ , for some  $n \geq 0$  and some  $P$  ((Sangiorgi & Walker, 2001), Lemma 15.3.22). Number  $n$  represents the nesting depth of the redex in  $M$ , that is, the number of applications one must cross to reach the redex, when descending the derivation tree of  $M$ . The optimized encoding obviates the  $\pi$ -steps necessary to reach the  $\pi$ -redex corresponding to the  $\lambda$ -redex.

*Corollary 33 (Adequacy of the process encoding)*

Let  $\llbracket \cdot \rrbracket$  be the map defined in figure 9.  $M \downarrow_v^n N$  implies  $\llbracket M \rrbracket \downarrow_d^{2n} \simeq \llbracket N \rrbracket$ .

*Proof*

From Theorem 32, and the fact that  $\simeq$  commutes with  $\rightarrow_d$  (proof of Proposition 14).  $\square$

Now for the Context machine.

*Theorem 34 (Adequacy of the context encoding)*

Let  $\llbracket \cdot \rrbracket$  be the map defined in figure 10.  $M \downarrow_v^n N$  implies  $\llbracket M \rrbracket \downarrow_d^{2n} \simeq \llbracket N \rrbracket$ .

*Proof*

By induction on  $n$ . When  $n$  is zero,  $M = N$ , and we are done. When  $n$  is positive,  $M$  is an application, say  $M_1M_2$ . Let  $M_1 \downarrow_v^{n_1} \lambda xL$ ,  $M_2 \downarrow_v^{n_2} V$ , and  $L\{V/x\} \downarrow_v^{n_3} N$ , with  $n = n_1 + n_2 + n_3 + 1$ . We have  $\llbracket M_1M_2 \rrbracket \stackrel{\text{def}}{=} (vr \llbracket M_1 \rrbracket^v \llbracket M_2 \rrbracket^w \bar{v}wr \mid r(u).\square, u)$ , and

$$\begin{aligned} vr \llbracket M_1 \rrbracket^v \llbracket M_2 \rrbracket^w \bar{v}wr \mid r(u).\square &\rightarrow_d^{2(n_1+n_2)} \simeq && \text{(Induction)} \\ vr \bar{v} \underline{v} := \underline{\lambda xL} \mid v w w := V \mid \bar{v}wr \mid r(u).\square &\rightarrow_d \sim_{gc} \\ vr v w w := V \mid \llbracket L\{c/x\} \rrbracket^{w'} \bar{r}w' \mid r(u).\square &\simeq && \text{(Subst. Lemma 21)} \\ vr \llbracket L\{V/x\} \rrbracket^{w'} \bar{r}w' \mid r(u).\square &\rightarrow_d^{2n_3} \simeq && \text{(Induction)} \\ vr \llbracket N \rrbracket^{w'} \bar{r}w' \mid r(u).\square &\stackrel{\text{def}}{=} \\ vr v w' w' := N \mid \bar{r}w' \mid r(u).\square &\rightarrow_d \equiv \\ vu u := N \mid \square & \end{aligned}$$

Finally  $(vu u := N \mid \square, u) \stackrel{\text{def}}{=} \llbracket N \rrbracket$ .  $\square$

### 4.6 Lambda to Pi, soundness

It is not difficult to show that both interpretations of the  $\lambda$ -calculus into the  $\pi$ -calculus are sound, by following Sangiorgi & Walker (2001).

For the equivalence in  $\lambda$ -terms (equipped with the call-by-value strategy), we pick the *Morris context equivalence*, also called *observation equivalence*.

*Definition 35*

Two closed  $\lambda$ -terms  $M, N$  are *observationally equivalent* if  $C[M] \downarrow_v$  iff  $C[N] \downarrow_v$ , for each closed  $\lambda$ -context  $C$ . In this case we write  $M \simeq_v N$ .

The equivalence for  $\pi$ -calculus *processes* is the weak version of barbed congruence, defined by replacing, in figure 5, reduction  $\rightarrow_d$  with the relation  $\rightarrow_d^*$ , and the observation predicate  $\downarrow_a$  with the predicate  $\rightarrow_d^* \downarrow_a$ . We write  $\cong_p$  for the relation thus obtained. The equivalence for  $\pi$ -calculus *contexts* is obtained from the strong version, by replacing, in figure 6,  $\simeq$  by  $\cong_p$ . We use symbol  $\cong_c$  for the relation thus obtained.

Then we can easily show soundness from the adequacy results of section 4.5, by following the proof of the same result for the unoptimized version of the process-encoding (Sangiorgi & Walker, 2001, Theorem 17.3.3).

*Theorem 36*

Let  $M, N \in \text{term}^0$ , and  $M \simeq_v N$ . Then,

1.  $\llbracket M \rrbracket \cong_p \llbracket N \rrbracket$ , for  $\llbracket \cdot \rrbracket$  the map defined in figure 9;
2.  $\llbracket M \rrbracket \cong_c \llbracket N \rrbracket$ , for  $\llbracket \cdot \rrbracket$  map defined in figure 10.

Completeness, on the other hand is not be expected to hold. See the discussion in Sangiorgi & Walker (2001).

## 5 Further work

The impact of the  $\pi$ -encodings proposed here on actual,  $\pi$ -based, programming languages should be analyzed (Pierce & Turner, 2000; Vasconcelos & Lopes, 1988–2002). Also, further optimizations for particular patterns of  $\lambda$ -terms (e.g. recursive function definitions) could be pursued.

The encodings of the environment machines opens perspectives of encoding others machines, thus providing for the study of other  $\lambda$ -reduction strategies in the realm of  $\pi$ -calculus. In this respect, the context encoding seems to be tightly connected to the call-by-value strategy, remaining unclear how to apply it to call-by-name. It remains open whether the two  $\lambda$ -encodings are barbed congruent.

Sangiorgi & Walker (2001) presents Milner's encoding using a continuation-passing style. It should be interesting to investigate whether there is a CPS transform that yields the encoding that goes via the SECD machine.

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